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9 APPENDIX

9.1 Equipment Details

9.1.1 Linear motor

Motor specifications

Continuous Force	60N (13.5lbs)
Continuous Current	1.3Arms
Acceleration Force	240N (54.0lbs)
Acceleration Current	5.1Arms
Force Constant (Kf)	47N/Arms (10.67lbs/amp)
Back EMF (Ke)	16V/m/s
Resistance 25°C	12Ω
Inductance	15mH
Electric Time Constant	1.25ms
Rated Voltage (AC)	240V
Fundamental Motor Constant (Km)	11.19N/V
Magnetic Pitch (North-North)	90mm (3.54lbs)
Usable stroke length	250mm
Shaft length	515mm
Shaft mass	1.6kg



Linear Motor [72]



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Dimensions of selected motor as shown in the figure 6.5,

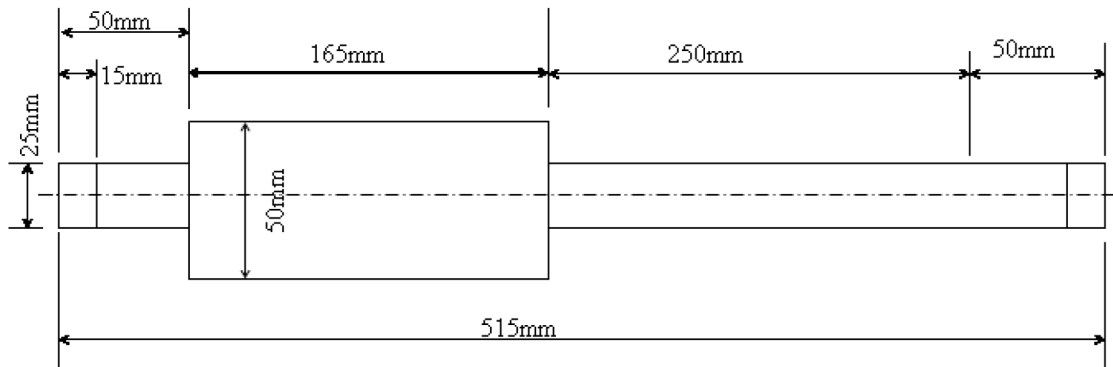


Figure 9.2: Motor Dimensions

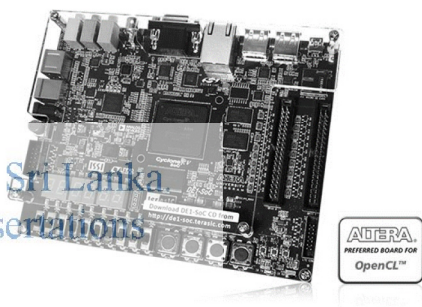
9.1.2 FPGA

Altera Terasic DE1 Soc field programmable gate array is used as the controller to provide high speed processing with following specifications

Cyclone V SoC 5CSEMA5F31C6 Device
 www.lib.mrt.ac.lk



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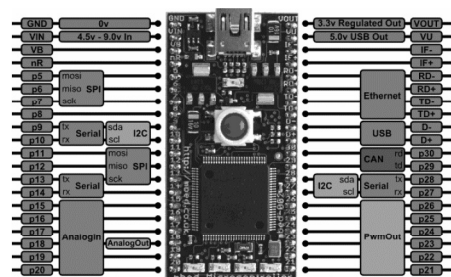
Terasic Altera DE1-SOC [73]

- 85K Programmable Logic Elements
- 4,450 Kbits embedded memory
- 6 Fractional PLLs

This controller provides 100MHz sampling and processing with a minimum amount of latency. Controller desing is included in the appendix.

9.1.3 Mbed Microcontroller

Mbed NXP LPC1768 microcontroller is used as secondary controller to control cam assembly. Mbed microcontroller is taking input signals mainly from accelerometer and rotary encoder and giving output PWM signals to rotary motor driver.



Mbed Microcontroller[74]

Mbed 32-bit ARM Cortex-M3 core is running at 96MHz. It includes 512KB FLASH, 32KB RAM and lots of interfaces including built-in Ethernet, USB Host and Device, CAN, SPI, I2C, ADC, DAC, PWM and other I/O interfaces.
 NXP LPC1768 MCU

- High performance ARM® Cortex™-M3 Core
- 96MHz, 32KB RAM, 512KB FLAS
- Ethernet, USB Host/Device, 2xSPI, 2xI2C, 3xUART, CAN, 6xPWM, 6xADC, GPIO

Prototyping form-factor

- 40-pin 0.1" pitch DIP package, 54x26mm
- 5V USB or 4.5-9V supply
- Built-in USB drag 'n' drop FLASH programmer

Mbed.org Developer Website

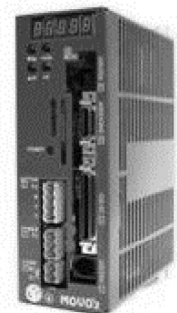
- Lightweight Online Compiler
- High level C/C++ SDK
- Cookbook of published libraries and projects

9.1.3.1.1 Linear Motor Driver

Linear Motor Driver is one of the key components of the system. It communicates with host controller (FPGA) and control linear motor according to signals. SVFH3-H3-DSP*SRI Servo land linear motor driver is selected as linear motor driver. It is capable to control motor current of the linear motor. Serial 2 phase current command type was added for the driver for that purpose.

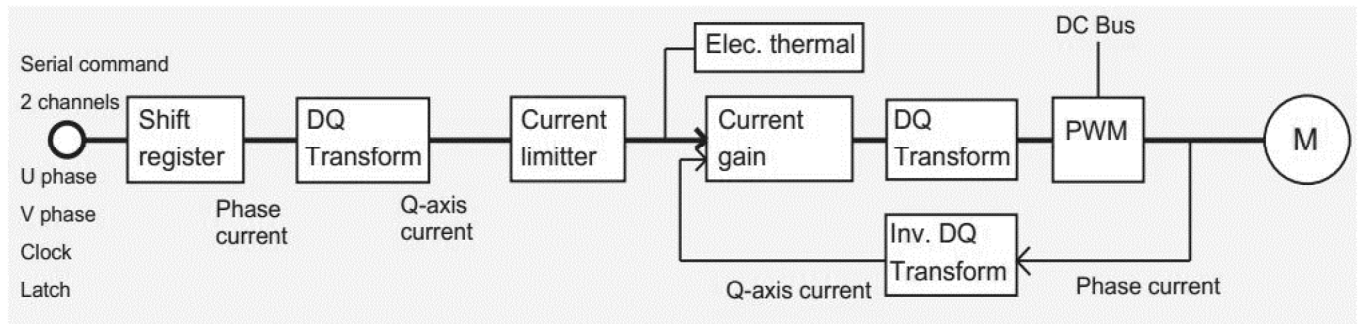
Principal specifications

- Motion mode - SRI Serial 2 phase current command mode
- Analog input port - 2 channels, Differential / Single end
- Input command data - 16 bit
- Control type - DQ Vector control
- A/D Resolution of current detection - 16 bit fast sigma-delta type
- PWM frequency - 15 to 40 kHz
- PWM Resolution - 7 ns
- Current control cycle - Synchronized with PWM frequency
- Current response - 3 kHz (DSP model), 1.5 kHz (Base model)
- Output - 436W



Linear Motor Driver [75]

Internal block diagram of the motor driver is as shown in the figure 6.9,



Internal Block Diagram of Linear Motor [76]

9.1.3.1.2 Linear Encoder

Linear encoder is the main and the only sensor which take measurements for controlling the system. Linear encoder signals are directly connected with FPGA. From the linear Encoder, relative displacement between sprung mass and un-sprung mass is measured. LM10 magnetic type linear encoder is selected from Renishaw. Company for the application.

System data



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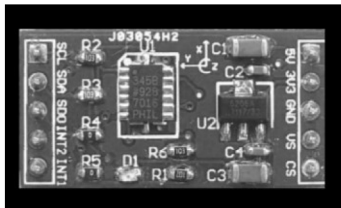
- Maximum length for MS scale 50 m (100 m special order)
- Pole length 2 mm
- Available resolutions 1 μm , 2 μm , 5 μm , 10 μm , 20 μm and 50 μm
- Sinusoidal period length 2 mm
- Maximum speed For a Precision class for MS scales $\pm 20 \mu\text{m/m}$ and $\pm 40 \mu\text{m/m}$
- Linear expansion coefficient for MS scale $\sim 17 \times 10^{-6}/\text{K}$
- Repeatability Better than unit of resolution for movement in the same direction
- Hysteresis $< 3 \mu\text{m}$ up to 0.5 mm ride height
- Sub divisional error $\pm 3.5 \mu\text{m}$ for $< 0.7 \text{ mm}$ ride height $\pm 7.5 \mu\text{m}$ for 1 mm ride height



Linear Encoder [77]

9.1.3.1.3 Accelerometer

Accelerometer is used for data collection purposes. Accelerometer is connected to Mbed microcontroller. For that ADXL345 3-Axis digital accelerometer is used. The ADXL345 is a small, thin, ultralow power, 3-axis accelerometer with high resolution (13-bit) measurement at up to ± 16 g. Digital output data is formatted as 16-bit twos complement and is accessible through either a SPI (3- or 4-wire) or I²C digital interface.



Accelerometer [78]

9.1.4 Mechanical equipment



Linear Guide [79]

Linear Bearings [80]

9.2 Main module code

This only includes the main module code of one version of implementation due to limitations of pages.

```

module ACTIVE_MODULE(
    //////////// CLOCK ////////////
    input                CLOCK_50,
    input                CLOCK2_50,
    input                CLOCK3_50,
    input                CLOCK4_50,
    output [6:0]        HEX5,

    //////////// KEY ////////////

```

```

        input          [3:0]      KEY,
        ////////////// LED ///////////
        output         [9:0]      LEDR,
        ////////////// SW ///////////
        input          [9:0]      SW,
        ////////////// GPIO_0, GPIO_0 connect to GPIO Default ///////////
        inout          [35:0]     GPIO_0,
        ////////////// GPIO_1, GPIO_1 connect to GPIO Default ///////////
        inout          [35:0]     GPIO_1
    ); //=====
    // REG/WIRE declarations
    //=====

    wire res; //async reset
    reg reset = 0; //sync reset
    wire enable; //enable

    //pll pins
    reg pll_reset = 0; //outside clock sync pll_reset
    wire [4:0] pll_cascade;
    wire pll_lock;
    wire pll_lock_2;
    wire CLOCK_1000;
    wire CLOCK_500;
    wire CLOCK_200;
    wire CLOCK_100;
    wire CLOCK_50_PLL;
    wire CLOCK_40;
    wire CLOCK_4;

    //reg input Key edge detection
    reg [3:0] KEY_prev = 0;
    wire [3:0] KEY_edge;

    //led register for sync led out
    reg [9:0] LED_OUT = 9'b000000000;

    //input switch logics
    wire SW_12_logic_00;

```




```
wire SW_12_logic_01;
wire SW_12_logic_11;
wire SW_12_logic_10;
wire SW_345_logic_000 ;
wire SW_345_logic_001;
wire SW_345_logic_010;
wire SW_345_logic_011 ;
wire SW_345_logic_100;
wire SW_345_logic_101 ;
wire SW_345_logic_110 ;
wire SW_345_logic_111;
wire SW_6_logic_0;
wire SW_6_logic_1;
//register read SPI logic
wire GPIO1_1357_logic_0000;
wire GPIO1_1357_logic_0001;
wire GPIO1_1357_logic_0010;
wire GPIO1_1357_logic_0011;
wire GPIO1_1357_logic_0100;
wire GPIO1_1357_logic_0101;
wire GPIO1_1357_logic_0110;
wire GPIO1_1357_logic_0111;
wire GPIO1_1357_logic_1000;
wire GPIO1_1357_logic_1001;
wire GPIO1_1357_logic_1010;
wire GPIO1_1357_logic_1011;
wire GPIO1_1357_logic_1100;
wire GPIO1_1357_logic_1101;
wire GPIO1_1357_logic_1110;
wire GPIO1_1357_logic_1111;
//wires for mbed spi slave GPIO connections
wire MBED_SCLK;
wire MBED_CS;
wire MBED_MOSI;
wire MBED_MISO;
```



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```
wire MBED_READY;
wire MBED_FINISH;
wire MBED_BUSY;
reg [15:0] MBED_DATAOUT = 16'b0;
wire [15:0] MBED_DATAIN;
reg [15:0] MBED_READ_REG = 16'b0;
reg [15:0] MBED_READ_REG_HIGH = 16'b0;
//adc read values
reg [15:0] ADC_DIN_VALUE = 16'b10_00_00_110_000_0000;
wire [15:0] ADC_DOUT_VALUE;
//input wires for movo drive GPIOs
wire movo_data_A;
wire movo_data_B;
wire movo_clk;
wire movo_latch;
wire movo_data_A_not;
wire movo_data_B_not;
wire movo_clk_not;
wire movo_latch_not;
wire data_status ;
//BLDC commutation
wire hall_1;
wire hall_2;
wire hall_3;
wire [15:0] current_U;
wire [15:0] current_V;
wire hall_error;
//quadrature encoder interface
wire encoder_phaseA;
wire encoder_phaseB;
wire [31:0] pulse_position;
wire pulse;
//force output to send to the drivers
reg [31:0] forceOutFloat = 0;
//calculated float values
```



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```

wire [31:0] position;
wire [31:0] acceleration;
wire [31:0] velocity;
wire[31:0] Kspring;
wire[31:0] Cdamper;
wire[31:0] Kp;
wire[31:0] Ki;
wire[31:0] Kd;
wire[31:0] mass;
wire[31:0] forceCurrentSense;
wire[31:0] forceRef;
wire[15:0] currentOutDAC;
wire[15:0] currentOutMOVO;
reg [31:0] forceRef_in = 0;
wire[31:0] forceRFOB;
wire[31:0] forceOutRFOB_act;
wire[31:0] forceOutSpring;
//raw integers of values
reg [15:0] forceRefRaw = 0;
reg [15:0] KpRaw = 950;
reg [15:0] KiRaw = 0;
reg [15:0] KdRaw = 0;
reg [15:0] massRaw = 110;
reg [15:0] KspringRaw = 1200;
reg [15:0] CdamperRaw = 0;
assign res = ~KEY[0];
assign enable = SW[0] & pll_lock;
// LED indicator assignmene
assign LEDR = LED_OUT;
//mbed spi slave connector ---check input pin filters
assign GPIO_1[2] = MBED_MISO; //output
//movo communication pins
assign GPIO_0[0] = movo_data_A;
assign GPIO_0[2] = movo_data_B;
assign GPIO_0[4] = movo_clk;

```



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```

assign GPIO_0[6] = movo_latch;
assign GPIO_0[1] = movo_data_A_not;
assign GPIO_0[3] = movo_data_B_not;
assign GPIO_0[5] = movo_clk_not;
assign GPIO_0[7] = movo_latch_not;
assign GPIO_0[8] = 1'b0;
assign GPIO_0[9] = 1'b0;

//check input filters

//DAC 712 ouput pins
assign GPIO_1[25:10] = currentOutDAC[15:0];

// positive edge detection of the input keys
assign KEY_edge[0] = ~KEY_prev[0] & KEY[0];
assign KEY_edge[1] = ~KEY_prev[1] & KEY[1];
assign KEY_edge[2] = ~KEY_prev[2] & KEY[2];
assign KEY_edge[3] = ~KEY_prev[3] & KEY[3];

//SW 0-9 logic connectors
assign SW_12_logic_00 = ~SW[1] & ~SW[2];
assign SW_12_logic_01 = ~SW[1] & SW[2];
assign SW_12_logic_11 = SW[1] & SW[2];
assign SW_12_logic_10 = SW[1] & ~SW[2];
assign SW_345_logic_000 = ~SW[3] & ~SW[4] & ~SW[5];
assign SW_345_logic_001 = ~SW[3] & ~SW[4] & SW[5];
assign SW_345_logic_010 = ~SW[3] & SW[4] & ~SW[5];
assign SW_345_logic_011 = ~SW[3] & SW[4] & SW[5];
assign SW_345_logic_100 = SW[3] & ~SW[4] & ~SW[5];
assign SW_345_logic_101 = SW[3] & ~SW[4] & SW[5];
assign SW_345_logic_110 = SW[3] & SW[4] & ~SW[5];
assign SW_345_logic_111 = SW[3] & SW[4] & SW[5];
assign SW_6_logic_0 = ~SW[6];
assign SW_6_logic_1 = SW[6];

//GPIO_1 [1-7] Mbed register read connection
assign GPIO1_1357_logic_0000 = ~GPIO_1[1] & ~GPIO_1[3] & ~GPIO_1[5] & ~GPIO_1[7];
assign GPIO1_1357_logic_0001 = ~GPIO_1[1] & ~GPIO_1[3] & ~GPIO_1[5] & GPIO_1[7];
assign GPIO1_1357_logic_0010 = ~GPIO_1[1] & ~GPIO_1[3] & GPIO_1[5] & ~GPIO_1[7];
assign GPIO1_1357_logic_0011 = ~GPIO_1[1] & ~GPIO_1[3] & GPIO_1[5] & GPIO_1[7];

```

```

assign GPIO1_1357_logic_0100 = ~GPIO_1[1] & GPIO_1[3] & ~GPIO_1[5] & ~GPIO_1[7];
assign GPIO1_1357_logic_0101 = ~GPIO_1[1] & GPIO_1[3] & ~GPIO_1[5] & GPIO_1[7];
assign GPIO1_1357_logic_0110 = ~GPIO_1[1] & GPIO_1[3] & GPIO_1[5] & ~GPIO_1[7];
assign GPIO1_1357_logic_0111 = ~GPIO_1[1] & GPIO_1[3] & GPIO_1[5] & GPIO_1[7];
assign GPIO1_1357_logic_1000 = GPIO_1[1] & ~GPIO_1[3] & ~GPIO_1[5] & ~GPIO_1[7];
assign GPIO1_1357_logic_1001 = GPIO_1[1] & ~GPIO_1[3] & ~GPIO_1[5] & GPIO_1[7];
assign GPIO1_1357_logic_1010 = GPIO_1[1] & ~GPIO_1[3] & GPIO_1[5] & ~GPIO_1[7];
assign GPIO1_1357_logic_1011 = GPIO_1[1] & ~GPIO_1[3] & GPIO_1[5] & GPIO_1[7];
assign GPIO1_1357_logic_1100 = GPIO_1[1] & GPIO_1[3] & ~GPIO_1[5] & ~GPIO_1[7];
assign GPIO1_1357_logic_1101 = GPIO_1[1] & GPIO_1[3] & ~GPIO_1[5] & GPIO_1[7];
assign GPIO1_1357_logic_1110 = GPIO_1[1] & GPIO_1[3] & GPIO_1[5] & ~GPIO_1[7];
assign GPIO1_1357_logic_1111 = GPIO_1[1] & GPIO_1[3] & GPIO_1[5] & GPIO_1[7];

//external clock sync reset
always @(posedge CLOCK_50) begin
    pll_reset <= ~KEY[0] & ~KEY[3];
end

//logic
always @(posedge CLOCK_200) begin
    reset <= res;
    KEY_prev[3:0] <= KEY[3:0];
    LED_OUT[0] <= enable;
    LED_OUT[1] <= reset;
    LED_OUT[6:2] <= SW[5:1];
    LED_OUT[7] <= hall_error;
    LED_OUT[8] <= pulse;
    LED_OUT[9] <= data_status;

    //LED_OUT = MBED_DATAIN[9:0];

    if(reset) begin
        forceRef_in <= 0;
        forceOutFloat <= 0;
        forceRefRaw <= 0;
        KpRaw <= 995;
        KiRaw <= 0;
        KdRaw <= 0;
    end
end

```



```

        KspringRaw <= 1200;
        CdamperRaw <= 1;
        forceOutFloat <= 0;
    end
end
// observations of values using mbed SPI
always @ (posedge CLOCK_200) begin
    if (reset)begin
        MBED_DATAOUT <= 16'b0;
        MBED_READ_REG <= 16'b0;
        MBED_READ_REG_HIGH <= 16'b0;
    end
    else begin
        if (enable)begin
            if(MBED_CS) begin
                MBED_DATAOUT <= MBED_READ_REG;
                if ( GPIO1_1357_logic_0000 ) begin
                    MBED_DATAOUT <= pulse_position[15:0];
                    MBED_READ_REG_HIGH <= pulse_position[31:16];
                end
                if ( GPIO1_1357_logic_0001 ) begin
                    MBED_DATAOUT <= velocity[15:0];
                    MBED_READ_REG_HIGH <= velocity[31:16];
                end
            end
            if ( GPIO1_1357_logic_0010 ) begin
                MBED_DATAOUT <= forceRFOB[15:0];
                MBED_READ_REG_HIGH <= forceRFOB[31:16];
            end
            if ( GPIO1_1357_logic_0011 ) begin
                MBED_DATAOUT <= forceOutRFOB_act[15:0];
                MBED_READ_REG_HIGH <= forceOutRFOB_act[31:16];
            end
            if ( GPIO1_1357_logic_0100 ) begin
                MBED_DATAOUT <= forceOutSpring[15:0];
                MBED_READ_REG_HIGH <= forceOutSpring[31:16];
            end
        end
    end
end

```




```

end

if ( GPIO1_1357_logic_0101 ) begin
    MBED_DATAOUT <= Kspring[15:0];
    MBED_READ_REG_HIGH <= Kspring[31:16];
end

if ( GPIO1_1357_logic_0110 ) begin
    MBED_DATAOUT <= Kp[15:0];
    MBED_READ_REG_HIGH <= Kp[31:16];
end

if ( GPIO1_1357_logic_0111 ) begin
    MBED_DATAOUT <= MBED_READ_REG_IIIIGII;
end

if ( GPIO1_1357_logic_1000 ) begin
    MBED_DATAOUT <= currentOutDAC;
    MBED_READ_REG_HIGH <= 16'b0;
end

if ( GPIO1_1357_logic_1001 ) begin
    MBED_DATAOUT <= ADC_DOUT_VALUE;
    MBED_READ_REG_HIGH <= 16'b0;
end

if ( GPIO1_1357_logic_1010 ) begin
    MBED_DATAOUT <= KpRaw;
    MBED_READ_REG_HIGH <= 16'b0;
end

if ( GPIO1_1357_logic_1011 ) begin
    MBED_DATAOUT <= KdRaw;
    MBED_READ_REG_HIGH <= 16'b0;
end

if ( GPIO1_1357_logic_1100 ) begin
    MBED_DATAOUT <= KiRaw;
    MBED_READ_REG_HIGH <= 16'b0;
end

if ( GPIO1_1357_logic_1101 ) begin
    MBED_DATAOUT <= KspringRaw;
    MBED_READ_REG_HIGH <= 16'b0;
end

```



```

end
if ( GPIO1_1357_logic_1110 ) begin
    MBED_READ_REG <= CdamperRaw;
    MBED_READ_REG_HIGH <= 16'b0;
end
if ( GPIO1_1357_logic_1111 ) begin
    MBED_READ_REG <= forceRefRaw;
    //MBED_DATAOUT <= MBED_DATAIN;
    MBED_READ_REG_HIGH <= 16'b0;
end
end
end
else begin
end
end
end
//input pin filters
/*
assign MBED_SCLK = GPIO_0[24]; //input
assign MBED_CS = GPIO_0[26]; //input
assign MBED_MOSI = GPIO_0[20]; //input
*/
filter_1_bit filter_1_bitMBED_SCLK(
    CLOCK_500,
    GPIO_1[4],
    MBED_SCLK
);
filter_1_bit filter_1_bitMBED_CS(
    CLOCK_500,
    GPIO_1[6],
    MBED_CS
);
filter_1_bit filter_1_bitMBED_MOSI(
    CLOCK_500,
    GPIO_1[0],

```



```

        MBED_MOSI
    );
    /*
    assign hall_1 = GPIO_0[10];
    assign hall_2 = GPIO_0[12];
    assign hall_3 = GPIO_0[14];
    */
    filter_1_bit filter_1_bithall_1(
        CLOCK_200,
        GPIO_0[30],
        hall_1
    );
    filter_1_bit filter_1_bithall_2(
        CLOCK_200,
        GPIO_0[32],
        hall_2
    );
    filter_1_bit filter_1_bithall_3(
        CLOCK_200,
        GPIO_0[34],
        hall_3
    );
    /*assign encoder_phaseA = GPIO_0[17];
    assign encoder_phaseB = GPIO_0[19];
    */
    filter_1_bit filter_1_bitencoder_phaseA(
        CLOCK_200,
        GPIO_0[27],
        encoder_phaseA
    );
    filter_1_bit filter_1_bitencoder_phaseB(
        CLOCK_200,
        GPIO_0[29],
        encoder_phaseB
    );

```



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```
//PLL connection
//module pll_main (  refclk,  rst, 50,40,4, locked );
pll_main pll_main_inst(
    CLOCK_50,
    pll_reset,
    CLOCK_50_PLL,
    CLOCK_200,
    CLOCK_500,
    pll_lock
);

//spi 16bit slave to tranfer data to mbed
spi_slave mbed_spi(
    CLOCK_500,
    reset,
    enable,
    MBED_SCLK,
    MBED_CS,
    MBED_MOSI,
    MBED_DATAOUT,
    MBED_MISO,
    MBED_DATAIN,
    MBED_READY,
    MBED_FINISH,
    MBED_BUSY
);

//encoder interface
quadrature_encoder quadrature_encoder_inst(
    .clk(CLOCK_200),
    .rst(reset),
    .A(encoder_phaseA),
    .B(encoder_phaseB),
    .up(),
    .down(),
    .pulse(pulse),
    .direction(),
```




```

        .pulse_count(pulse_position)
    );
//data converter module
data_converter data_converter_inst(
    .clk(CLOCK_200),
    .rst(reset),
    .enable(enable),

    .positionRaw(pulse_position),
    .accelerationRaw(MBED_DATAIN),
    .KspringRaw(KspringRaw),
    .CdamperRaw(CdamperRaw),
    .KpRaw(KpRaw),
    .KiRaw(KiRaw),
    .KdRaw(KdRaw),
    .massRaw(massRaw),
    .senseCurrentRaw(ADC_DOUT_VALUE),
    .forceRefRaw(forceRefRaw),
    .forceFloat(forceOutFloat),
    .position(position),
    .velocity(velocity),
    .acceleration(acceleration),
    .Kspring(Kspring),
    .Cdamper(Cdamper),
    .Kp(Kp),
    .Ki(Ki),
    .Kd(Kd),
    .mass(mass),
    .forceCurrentSense(forceCurrentSense),
    .forceRef(forceRef),
    .currentOutDAC(currentOutDAC),
    .currentOutMOVO(currentOutMOVO)
);
//active RFOB connection
activeRFOB activeRFOB_inst(

```



```

        .clk(CLOCK_200),
        .rst(reset),
        .enable(enable),
        .acceleration(acceleration),
        .position(position),
        .velocity(velocity),
        .senseForce(forceCurrentSense),
        .Kp(Kp),
        .Ki(Ki),
        .Kd(Kd),
        .mass(mass),
        .forceRef(forceRef_in),
        .forceRFOB(forceRFOB),
        .forceOutFloat(forceOutRFOB_act)
    );

```

//active spring damper connection

```

activeVirtualSpring activeVirtualSpring_inst(

```

```

        .clk(CLOCK_200),
        .rst(reset),
        .enable(enable),
        .position(position),
        .velocity(velocity),
        .K(Kspring),
        .C(Cdamper),
        .forceOutFloat(forceOutSpring)

```

```

    );

```

//BLDC commutation

```

BLDC_commutation BLDC_commutation_inst(

```

```

        .clk(CLOCK_200),
        .rst(reset),
        .enable(enable),
        .hall_1(hall_1),
        .hall_2(hall_2),
        .hall_3(hall_3),
        .current_in(currentOutMOVO),

```



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```
.current_out_U(current_U),
.current_out_V(current_V),
.hall_error(hall_error)
);

//Movo interface
movo_interface movo_interface_inst(
    .clk(CLOCK_50_PLL),
    .rst(reset),
    .enable(enable),
    .value_A(current_U),
    .value_B(current_V),
    .clk_movo(movo_clk),
    .clk_movo_not(movo_clk_not),
    .data_A(movo_data_A),
    .data_A_not(movo_data_A_not),
    .data_B(movo_data_B),
    .data_B_not(movo_data_B_not),
    .latch(movo_latch),
    .latch_not(movo_latch_not),
    .status(),
    .data_status(data_status)
);

endmodule
```

