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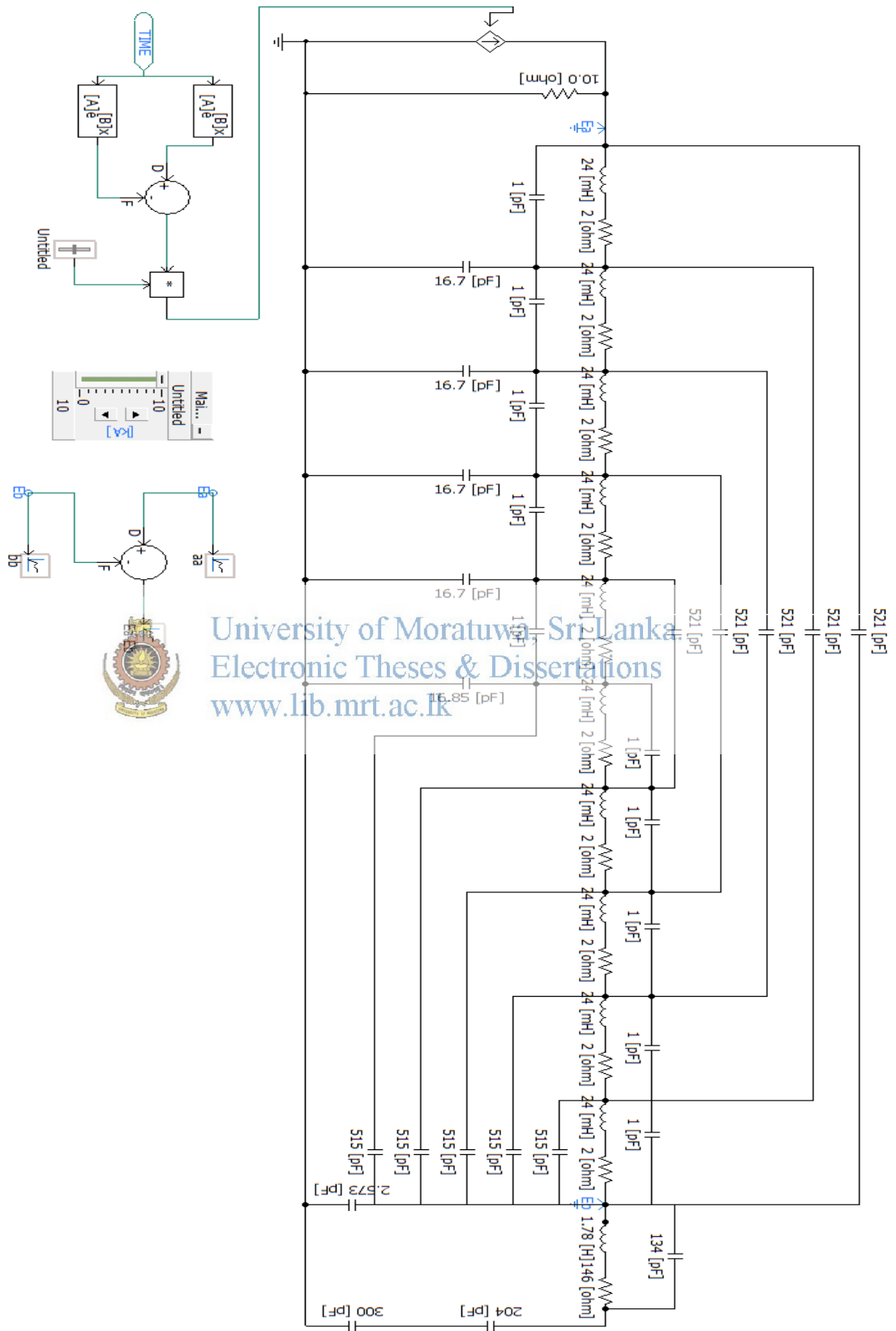


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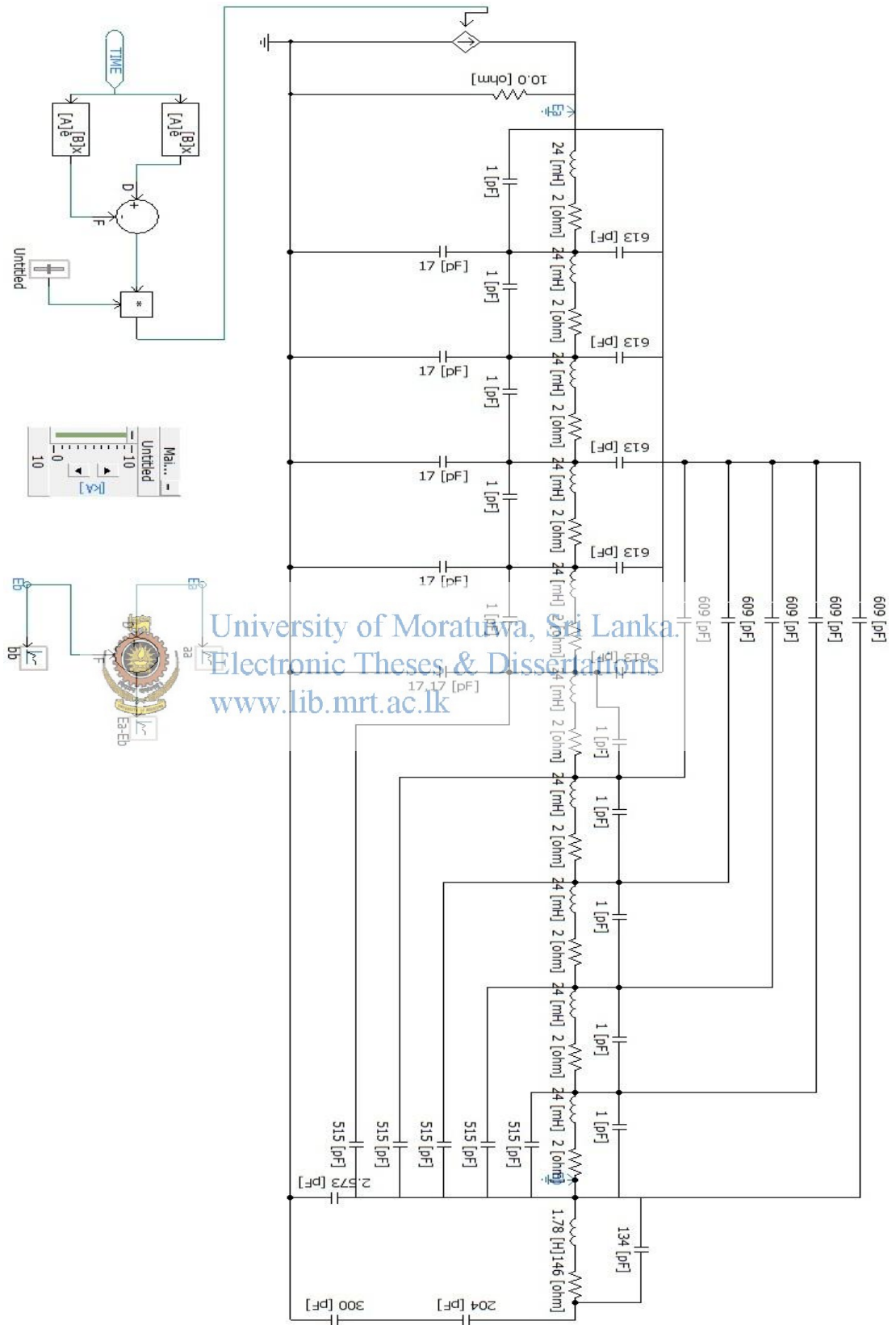
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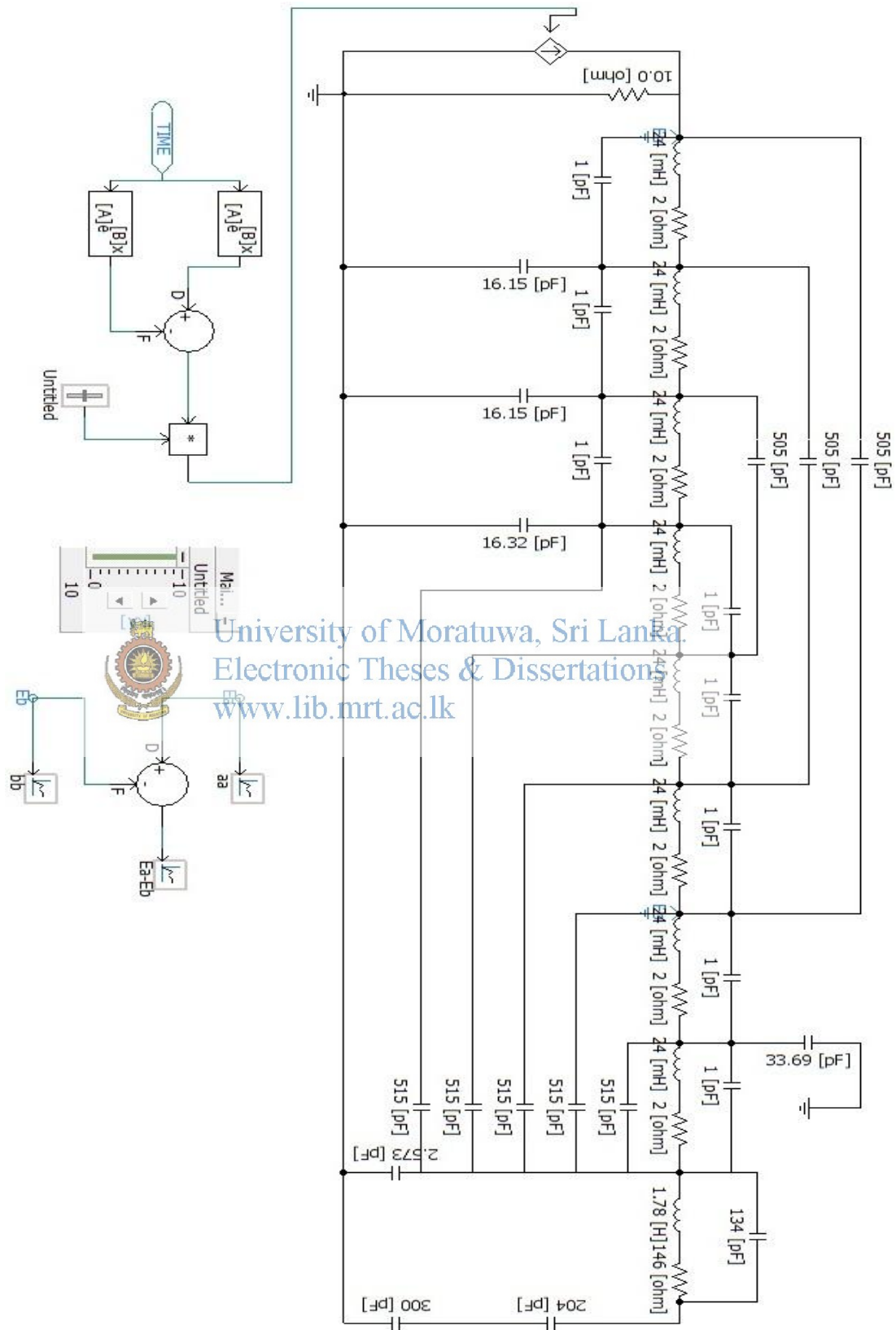
Appendix A: Simulation circuit for a HV winding having a fully filled outermost layer without shield



Appendix B: Simulation circuit for a HV winding having a fully filled outermost layer with shield



Appendix C: Simulation circuit for a HV winding having a partially filled outermost layer without shield



Appendix D: Simulation circuit for a HV winding having a partially filled outermost layer with shield

